## GAYATRI VIDYA PARISHAD COLLEGE OF ENGINEERING (AUTONOMOUS)

(Approved by AICTE, Affiliated to JNTU-Kakinada) Madhurawada, Visakhapatnam-530048

## M.Tech - II Semester Supplementary (Autonomous) Examinations - November 2020

(For 2018, 2017 & 2016 Admitted Batches)

|                          | TIME TABLE   |  |   |   |  | Time: 10.30 AM to 01.30 PM  |  |
|--------------------------|--|--|---|---|--|---|--|
| Date                     | STRUCTURAL<br>ENGINEERING<br>(CIVIL)   | INFRASTRUCTURE<br>ENGINEERING AND<br>MANAGEMENT<br>(CIVIL)                       | CYBER SECURITY<br>(CSE)                                   | COMMUNICATION<br>ENGINEERING & SIGNAL<br>PROCESSING<br>(ECE)            | VLSI DESIGN & EMBEDDED SYSTEMS (ECE)                                       | POWER ELECTRONICS<br>AND DRIVES<br>(EEE)                            | POWER SYSTEM AND<br>CONTROL AUTOMATION<br>(EEE)            |
| 01-12-2020<br>(Tuesday)  | Earthquake Resistant<br>Design of Structures<br>(15CE2210)                       |  |   |   |  |   | Power System Stability<br>(15EE2107)                       |
| 03-12-2020<br>(Thursday) | Advanced Foundation<br>Engineering<br>(15CE2211)                                 |  |   |   |  |   | Restructured Power<br>Systems (15EE2108)                   |
| 05-12-2020<br>(Saturday) | Elective-II Ports & Harbour Structures (15CE2115) (Common to Infra & Structural) | Elective-II Ports & Harbour Structures (15CE2115) (Common to Infra & Structural) |   | DSP Processors and<br>Architecture (15EC2113)<br>(Common to CSP & VLSI) | DSP Processors and<br>Architecture (15EC2113)<br>(Common to CSP &<br>VLSI) | Power Quality<br>Management (15EE2214)<br>(Common to PSCA &<br>PED) | Power Quality Management (15EE2214) (Common to PSCA & PED) |
| 08-12-2020<br>(Tuesday)  |  |  |   | Advanced Mobile<br>Communications (15EC2111)                            |  |   | Power System<br>Optimization<br>(15EE2109)                 |
| 10-12-2020<br>(Thursday) |  |  | Elective-II<br>Wireless and Mobile<br>Security (15CS2216) |   |  | HVDC & FACTS<br>(15EE2212)<br>(Common to PSCA &<br>PED)             | HVDC & FACTS<br>(15EE2212)<br>(Common to PSCA &<br>PED)    |

Note: (i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY.

<sup>(</sup>ii) FOR ANY OTHER CLARIFICATION IN RESPECT OF THE ABOVE EXAMINATIONS PLEASE CONTACT THE CONTROLLER OF EXAMINATIONS.